

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10060086			
<b>Filing Date:</b>	31-Jan-2002			
<b>Title of Invention:</b>	RISC MICROPROCESSOR ARCHITECTURE IMPLEMENTING MULTIPLE TYPED REGISTER SETS			
First Named Inventor/Applicant Name:	Sanjiv Garg			
<b>Filer:</b>	Donald J. Featherstone			
<b>Attorney Docket Number:</b>	2047.0600007			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	790	790
Total in USD (\$)				790